

The rejection of claims 1-26 under 35 U.S.C. § 102(e) as being anticipated by Moon is respectfully traversed and reconsideration is requested.

Claim 1 is allowable over the cited references in that claim 1 recites a combination of elements including, for example, “level shifting means... to apply a first voltage level for turning off the thin film transistors to the gate lines upon power-on and to apply a higher voltage level than the ground voltage to the gate lines upon power-off.” The cited references including Moon, do not teach or suggest each and every element of the claimed invention. Accordingly, Applicants respectfully submit that claim 1 and claims 2-8, which depend from claim 1, are allowable over the cited references.

Claim 9 is allowable over the cited references in that claim 9 recites a combination of elements including, for example, “receiving a power supply voltage and a ground voltage to apply a first voltage level for turning off the thin film transistors to the gate lines upon power-on.” The cited references including Moon, do not teach or suggest each and every element of the claimed invention. Accordingly, Applicants respectfully submit that claim 9 and claim 10, which depends from claim 9, are allowable over the cited references.

Claim 11 is allowable over the cited references in that claim 11 recites a combination of elements including, for example, “a gate voltage generator having a transistor connected between a first voltage source and a second voltage source to generate a gate-off voltage at an output.” The cited references including Moon, do not teach or suggest each and every element of the claimed invention. Accordingly, Applicants respectfully submit that claim 11 and claims 12-18, which depend from claim 11, are allowable over the cited references.

Claim 19 is allowable over the cited references in that claim 19 recites a combination of elements including, for example, “a gate-off voltage generator having a transistor

connected between a first voltage source and a second voltage source to generate a gate-off voltage at an output.” The cited references including Moon, do not teach or suggest each and every element of the claimed invention. Accordingly, Applicants respectfully submit that claim 19 and claims 20-26, which depend from claim 19, are allowable over the cited references.

The Examiner cites Moon as teaching “a screen clearing circuit 40 connected at an input to the gate driving circuit 10... When the external power is disconnected, the screen clearing circuit 40 operates to discharge the storage capacitors 80 connected to the gate lines (column 4, lines 27-29).” Applicants respectfully submit, however, Moon does not teach or suggest at least the aforementioned claimed elements at least with respect to claims 1, 9, 11, and 19.

Applicants believe the application is in condition for allowance and early, favorable action is respectfully solicited. Should the Examiner deem that a telephone conference would further the prosecution of this application, the Examiner is invited to call the undersigned attorney at (202) 496-7500.

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Page 4

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136. Please credit any overpayment to deposit Account No. 50-0911.

Respectfully submitted,

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